

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants: Y. NITTA, et al.
Application No.: Rule 1.53(b) Continuation of U.S. Patent Application
Serial No. 10/238,716, filed September 11, 2002
Filed: On even date herewith
For: SEMICONDUCTOR INTEGRATED CIRCUIT DEVICE
Art Group of Parent: 2816
Examiner of Parent: T. Lam

PRELIMINARY AMENDMENT

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

August 27, 2003

Sir:

Prior to examination, please amend the above-identified application as listed below and as set forth on the following pages:

Amendments to the Specification

Remarks are included following the amendments.